

# Logic Analysis & Monitoring

## Programmable Logic Device Emulator & Learning Kit

Detailed product specifications are available on: [us.100y.com.tw](http://us.100y.com.tw)



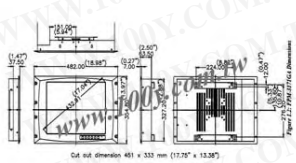
CPLD



DE2-70



FPGA3128B30



FPM-3171GA

Part No.	Product No.	Manufacturer	Description
35866	CPLD		CPLD
50803	DE2-70	ALTERA	Aletera DE2-70_Development and Education Board
23694	FPGA3128B30		FPGA 3.0 IC
40804	FPM-3171GA	ADVANTECH	FPM CPLD/FPGA



### FEATURES

- Module design.
- User can choose the chip board modules.
- Expensive chip board can re-used.
- Saving time without soldering QFP package.

Part No.	Product No.	Manufacturer	Description
12353	FPT-2	LEAP	Programmable Logic Device Emulator



### FEATURES

- Exploit CPLD/FPGA hardware/software development system to learn the newest design of logical IC to instead of the complex hard-ware design of TTL/CMOS.
- Capable to use Circuit Graphic and VHDL to develop hardware circuit.
- Directly download the designed program from the development system to CPLD via printer port to operate independently.

### Specifications

- Support Altera CPLD MAX7000S devices series
  - 1.EPM7064/32SLC44-10(alternative)
  - 2.PLD on EEPROM structure
  - 3.5Vworking voltage
  - 4.Support 1250 logic gates and 64 Lcs
  - 5.32 I/O available
- System clock:1.8432MHz
- ISP programming interface

Part No.	Product No.	Manufacturer	Description
17707	FPT-3	LEAP	CPLD/FPGA Programmable Logic Device Emulator (FPT-3 + EPM7064 PLCC44 IC)



Part No.	Product No.	Manufacturer	Description
12357	FPT-XCS20PQ208	LEAP	CPLD/FPGA IC Board



### FEATURES

- Use the software development environment provided by CPLD/FPGA in studying latest logic design, substitute the complicated hardware design of TTL/CMOS
- Capable to use circuit graphic and digital hard-ware descriptive syntax (VHDL/AHDL) to de-velop circuit.
- CPLD/FPGA provides free setting of pin, thus no hardware connection is required in case of test experience, save a lot of time for connection soldering, learn hardware operation promptly.
- Every I/O pin of CPLD/FPGA has logic status monitor, in order to facilitate speed understanding of each pin status.
- Clearly indicate the location of each pin, easy to visual and measure.
- Use print out to down load directly from manufacturer development system.
- Burn in CPLD/FPGA program into EPROM, the experimental device can operate stand-alone.
- Capable to perform 8051 and CPLD/FPGA match circuit test.
- Applicable for WIN95/98/NT, a series of item and document CAI, advance gradually, easy to understand and learn.

Part No.	Product No.	Manufacturer	Description
36605	LP-2900	LEAP	CPLD LP2900 ALTERA



P801



S3C2410X

Part No.	Product No.	Description
32925	P801	FPGA Development Kit
35868	S3C2410X	Study ARM9

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