

# Logic Analysis & Monitoring

## ZEROPLUS Logic Analyzer

Detailed product specifications are available on: [us.100y.com.tw](http://us.100y.com.tw)



### SPECIFICATIONS

Sample rate (Internal clock): 200MHz asynchronous operation  
 Sample rate (External clock): 100MHz synchronous operation  
 Channels may be selectively defaulted to /  
 Channels: 16 CH  
 Upgrade Oscilloscope to measure and decode I2C / UART (RS232C & RS485) / SPI Protocols and mixed signals  
 Glitch Capture Capability: 5ns  
 Bandwidth: 75MHz  
 Data decoding formats: Binary / Decimal / Hexadecimal / ASCII  
 Export data: Export to TXT or CSV files for processing  
 Compression rate: up to 255:1 depending on activity.  
 Power: USB-powered-ideal for PC and laptops  
 Trigger conditions: Flexible signal trigger options (rising edge, falling edge, either edge, high and low)  
 Selectable trigger position: Between 100% pre-trigger and 100% post trigger plus up to 16.7 million clocks of post-trigger delay  
 Channels may be selectively defaulted to "don't care" state.  
 Easy-to-use Windows application  
 Timing/State/Single-processor/ Bus/Real-time instruction trace analysis  
 Software updates available from ZEROPLUS website  
 Certificates: CE, FCC, WEEE, RoHS

Part No.	Product No.	Manufacturer	Description	Bandwidth(MHz)	Channel
37871	LAP-16128U	ZEROPLUS	16CH Logic Analyzer	75MHz	16



### SPECIFICATIONS

Sample rate (Internal clock): 200MHz asynchronous operation  
 Sample rate (External clock): 100MHz synchronous operation  
 Memory Depth (per channels): 128Kbits  
 Channel: 32CH  
 Upgrade Oscilloscope: to measure and decode I2C / UART (RS232C & RS485) / SPI Protocols and mixed signals  
 Clitch Capture Capability: 5ns  
 Bandwidth: 75MHz  
 Data decoding formats: Binary / Decimal / Hexadecimal / ASCII  
 Channels may be selectively defaulted to "don't care" state.  
 Compression rate: up to 255:1 depending on activity  
 Power: USB-powered-ideal for PC and laptops  
 Trigger conditions: Flexible signal trigger options (rising edge, falling edge, either edge, high and low).  
 Selectable trigger position: Between 100% pre-trigger and 100% post trigger plus up to 16.7 million clocks of post-trigger delay.  
 Channels may be selectively defaulted to "don't care" state  
 Easy-to use Windows application  
 Timing/State/Single-processor/ Bus/Real-time instruction trace analysis.  
 Software update available from ZEROPLUS website.  
 Certificates: CE, FCC, WEEE, RoHS

Part No.	Product No.	Manufacturer	Description	Bandwidth(MHz)	Channel
43518	LAP-32128U-A	ZEROPLUS	Logic Analyzer	75MHZ	32

## Leaptronix Logic Analyzer

Detailed product specifications are available on: [us.100y.com.tw](http://us.100y.com.tw)



Part No.	Product No.	Manufacturer	Description	Bandwidth (MHz)	Channel	Timing analysis
50767	LA-100P	LEAP	"Leaptronix" Logic Analyzer	100MHz	32CH	250MHz,max(4ns)
50765	LA-2025	LEAP	"Leaptronix" Logic Analyzer	200MHz	32CH	250MHz,max(4ns)
50766	LA-2050	LEAP	"Leaptronix" Logic Analyzer	200MHz	32CH	500MHz,max(2ns)



Part No.	Product No.	Manufacturer	Description	Bandwidth (MHz)	Channel	Timing analysis
50763	PLA-1016	LEAP	"Leaptronix" PC BASE Logic Analyzer	100MHz	16CH	100MHz,max(10ns)
50764	PLA-2532	LEAP	"Leaptronix" PC BASE Logic Analyzer	100MHz	32CH	250MHz,max(4ns)

- We provide a wide range of products to development and maintenance engineers in all type of business
- If you cannot find the components you want on the catalogue, please call us.



Taiwan: 886-3-5753170

Shanghai: 021-54151736 Shenzhen: 0755-83298787

**Centenary Materials**

Waiting for your phone call



T E L : Taiwan: 886-3-5753170  
 F A X : Taiwan: 886-3-5753172  
 E-mail: Taiwan: [us\\_sale@100y.com.tw](mailto:us_sale@100y.com.tw)

Shenzhen: 86-755-83298787  
 Shenzhen: 86-755-83640655  
 Shenzhen: [100y@163.com](mailto:100y@163.com)

Shanghai: 86-21-54151736  
 Shanghai: 86-21-64605107  
 Shanghai: [100y-1@163.com](mailto:100y-1@163.com)

57-03